

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/648,585
Priority Filing Date August 25, 2000
Inventor Belford T. Coursey
Assignee Micron Technology, Inc.
Priority Group Art Unit 2813
Priority Examiner Y. Huynh
Attorney's Docket No. MI22-1660
Title: Memory Circuitry, and Dynamic Random Access Memory Circuitry
(As Amended)

PRELIMINARY AMENDMENT

To: Box PATENT APPLICATION
Assistant for Patents and Trademarks
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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AMENDMENTS

In the Title

Please replace the title with the following:

--Memory Circuitry, and Dynamic Random Access Memory Circuitry--.

In the Specification

At p. 1, before the "Technical Field" section, please insert the following:

--RELATED PATENT DATA

This patent resulted from a divisional application of U.S. Patent Application Serial No. 09/648,585, filed on August 25, 2000, entitled "Method of Forming Memory Circuitry" (as amended), naming Belford T. Coursey as inventor, the disclosure of which is incorporated by reference.--

EL465780142US

In The Claims

Please cancel claims 1-25 without prejudice.

REMARKS

This application is a divisional application of U.S. Patent Application Serial No. 09/648,585. Original claims 1-25 have been canceled without prejudice. Claims 26-47 remain in the application for consideration.

Respectfully submitted,

Dated: 3-14-01

By: 

Mark S. Matkin
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